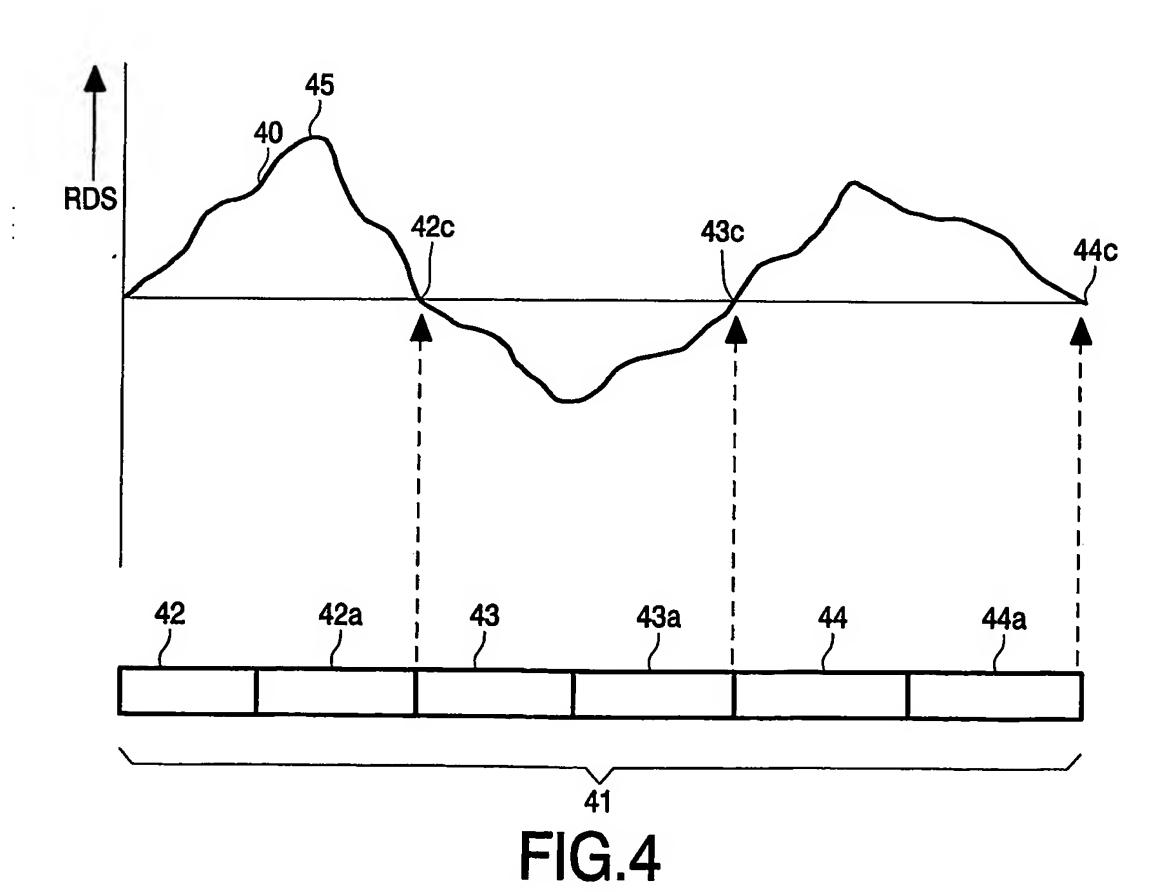


FIG.3



IN	OUT	
00	1010	
01	0001	
10	0111	
11	0101	
1010	00001000	

FIG.5

IN	OUT	
1010	00	
0001	01	
0111	10	
0101	11	
00001000	1010	

FIG.6

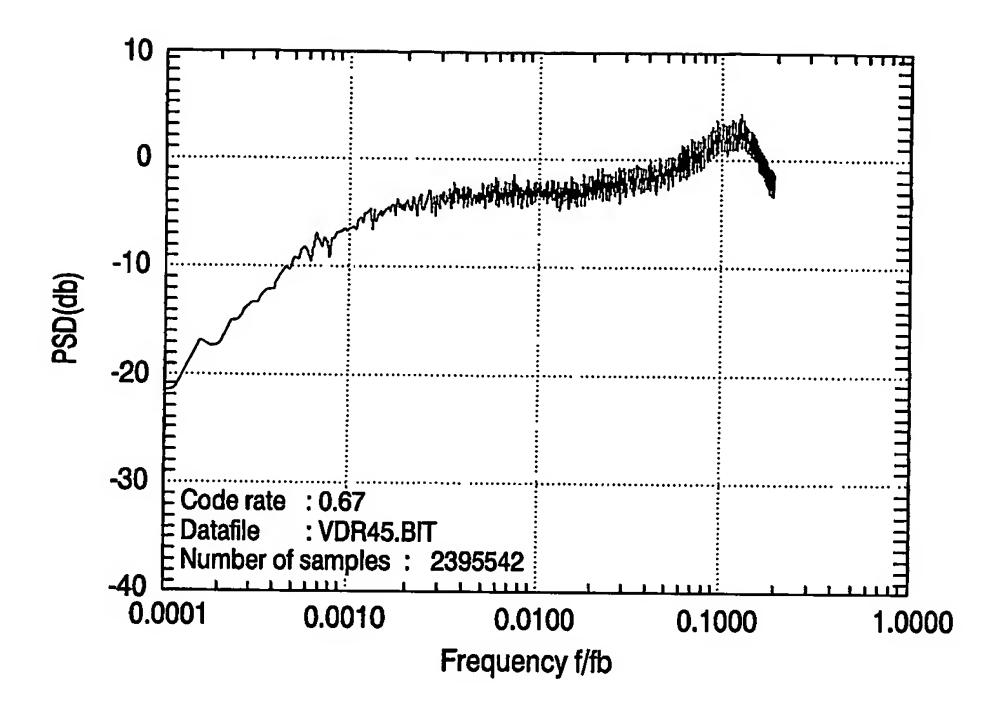


FIG.7

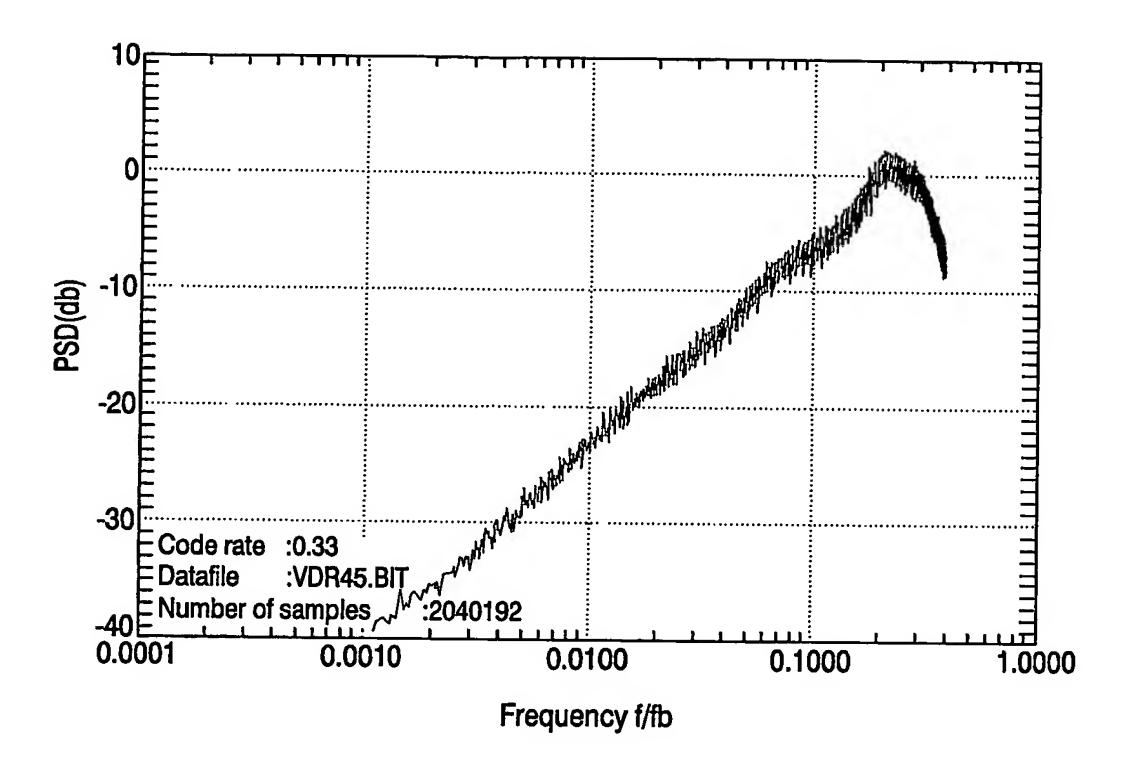


FIG.8

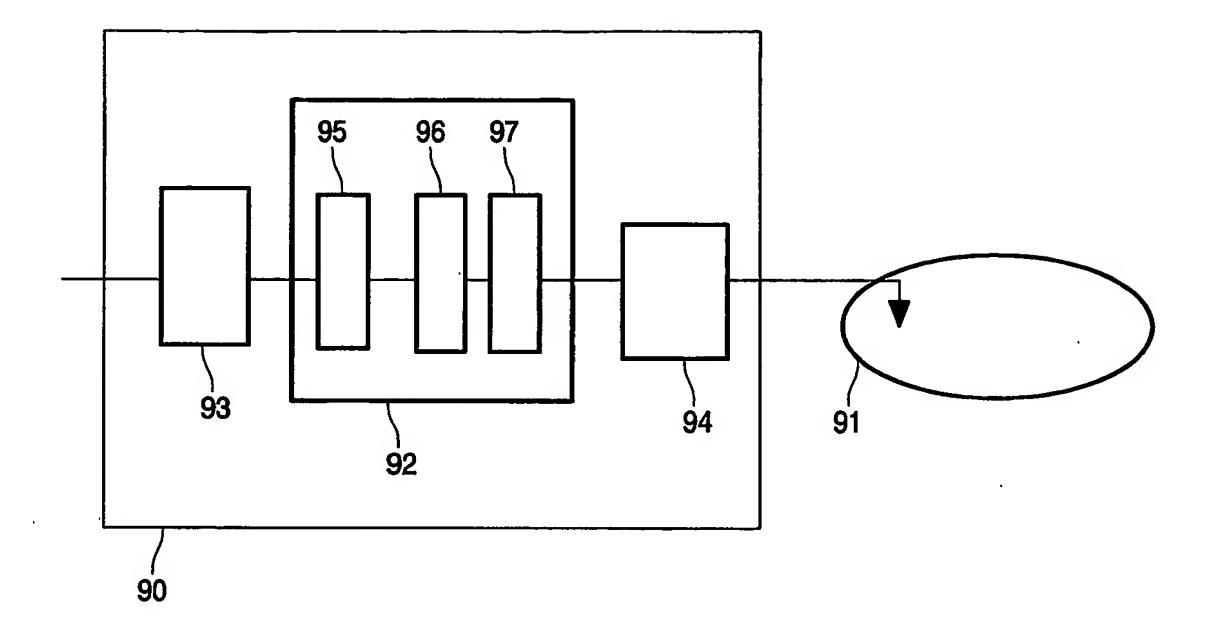


FIG.9

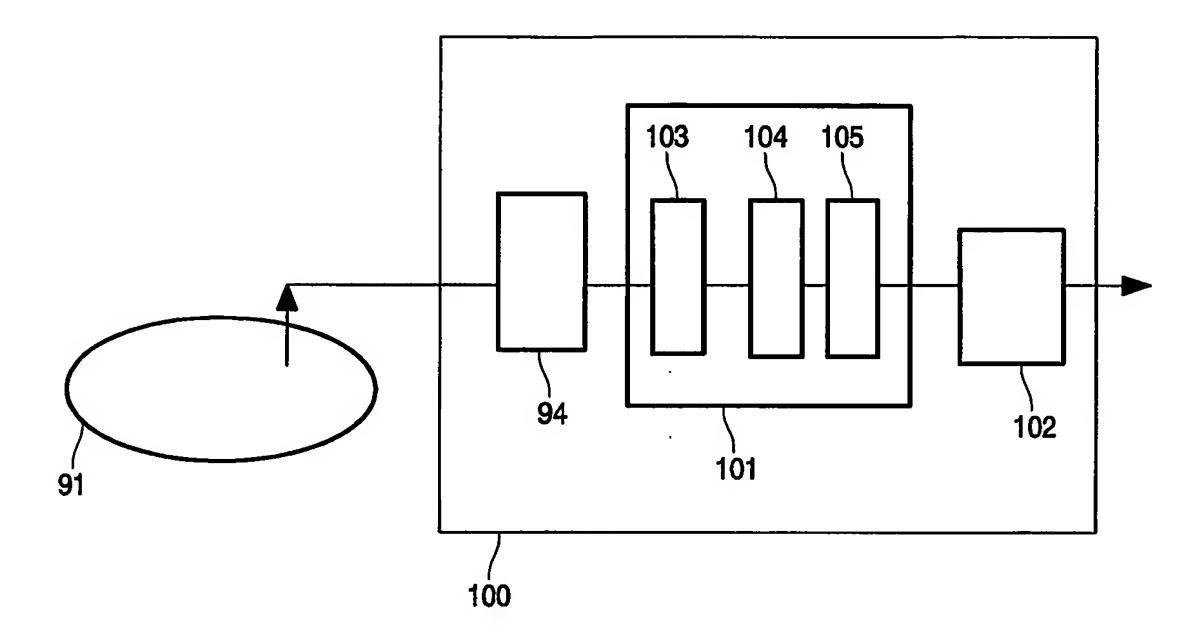
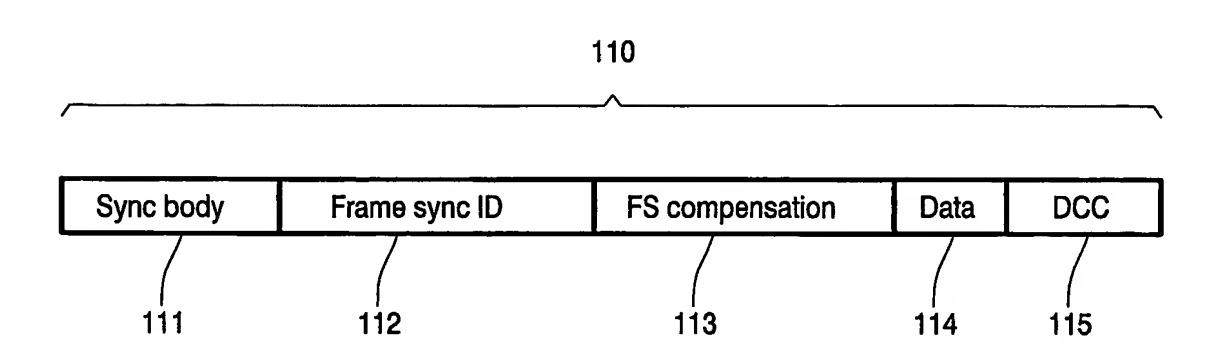


FIG.10



Sync body	Frame sync ID	FS compensation
X01010000000010000000010	000 001 —	
X0101000000010000010		→ \$EB
	010 010	→ \$BA
	101 000	→ \$BF
	100 001 —	→ \$DA
	000 100	→ \$3A
	001 001 —	→ \$BE
	010 000	→ \$B5

FIG.11